IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

HASEGAWA, et al.

Application No.:

TBD

Filed:

October 1, 2003

For:

METHOD OF MANUFACTURING SEMICONDUCTOR

INTEGRATED CIRCUIT DEVICE, OPTICAL MASK THEREFOR,

ITS MANUFACTURING METHOD, AND MASK BLANKS

Expected

Group:

1756

Expected

Examiner:

Saleha R. Mohamedulla

LETTER

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 October 1, 2003

Sir:

The above-identified application, which is a Continuation Application of prior Application No. 09/646,036, this Continuation application being filed concurrently herewith, is noted. The claims of this Continuation application are claims 1-37 of prior Application No. 09/646,036 as filed, and these claims 1-37 are being submitted in order to provide a proper non-provisional application as the above-identified Continuation application. Applicants will be filing a Supplemental Preliminary Amendment in due course, amending the original claims of the above-identified Continuation application. If this Supplemental Preliminary Amendment is not in the file of the above-identified application at the time that the Examiner takes up the above-identified application for consideration, the Examiner is respectfully requested

501.39055CX1

to contact the undersigned to determine the status of such Supplemental Preliminary Amendment. The Examiner is thanked in advance for cooperating with this request.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Case No. 501.39055CX1), and please credit any excess fees to such Deposit Account.

Respectfully submitted,

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WIS/sjg